

**ABSTRACT**

The present invention relates to a communications system and method, an information processing apparatus and method, as well as a program, which make it possible to perform highly precise clock synchronization with ease. A synchronization master apparatus 12 generates a synchronization control frame at  $t_1$ , waits for duration  $A_1$ , starts transmission of the synchronization control frame at  $t_2$  at which point the value of the clock counter register becomes "0," and resets the clock counter register at  $t_3$  at which point transmission of the synchronization control frame is completed. On the other hand, a synchronization slave apparatus 13 starts reception of the synchronization control frame at the same timing as  $t_2$ , completes reception of the synchronization control frame at the same timing as  $t_3$ , and resets the clock counter register. The present invention may be applied to an AV data communications system that transmits and receives real time data such as video data, audio data and the like.